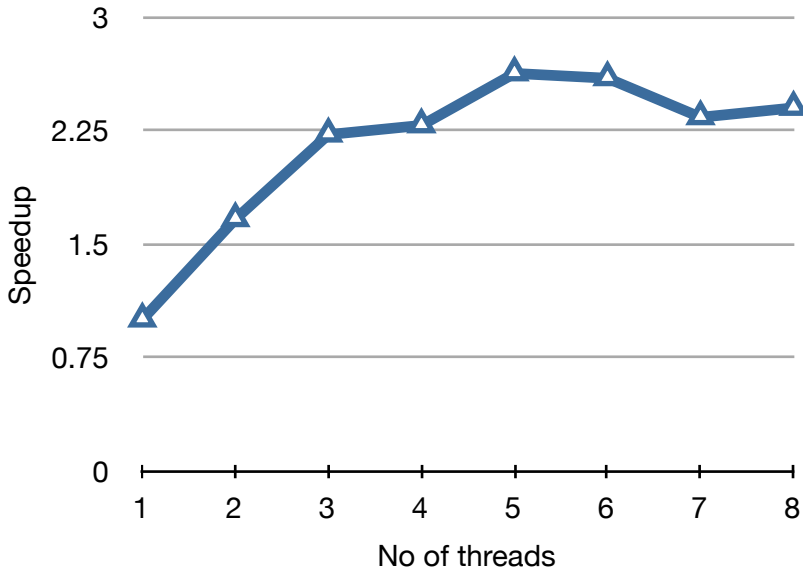


## Speedup of Laplace on 2x Quad-Core 3GHz Xeon



▲ Relative speedup, matrix size 400 x 400, 5000 iterations

